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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/829,235	04/22/2004	Takashi Ohtsuka	63979-042	7116
7590 02/22/2005			EXAMINER	
McDERMOTT, WILL & EMERY			LE, THONG QUOC	
600 13th Street, N.W. Washington, DC 20005-3096			ART UNIT	PAPER NUMBER
G ,			2827	
			DATE MAILED: 02/22/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)		
	10/829,235	OHTSUKA ET AL.		
Office Action Summary	Examiner	Art Unit		
	Thong Q. Le	2827		
The MAILING DATE of this communication	on appears on the cover sheet w	vith the correspondence address		
Period for Reply A SHORTENED STATUTORY PERIOD FOR ITHE MAILING DATE OF THIS COMMUNICAT - Extensions of time may be available under the provisions of 37 after SIX (6) MONTHS from the mailing date of this communicated if the period for reply specified above is less than thirty (30) day if NO period for reply is specified above, the maximum statutory - Failure to reply within the set or extended period for reply will, be any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	CION. CFR 1.136(a). In no event, however, may a tion. s, a reply within the statutory minimum of thi period will apply and will expire SIX (6) MO y statute, cause the application to become A	reply be timely filed irty (30) days will be considered timely. NTHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).		
Status				
1) Responsive to communication(s) filed or	١ <u></u> .			
2a) This action is FINAL. 2b) ∑	This action is FINAL. 2b)⊠ This action is non-final.			
3) Since this application is in condition for a closed in accordance with the practice u	· · · · · · · · · · · · · · · · · · ·			
Disposition of Claims				
4) ☐ Claim(s) 1-19 is/are pending in the application 4a) Of the above claim(s) is/are with 5) ☐ Claim(s) 4-19 is/are allowed. 6) ☐ Claim(s) 1 is/are rejected. 7) ☐ Claim(s) 2 and 3 is/are objected to. 8) ☐ Claim(s) are subject to restriction	ithdrawn from consideration.			
Application Papers	•			
9)☐ The specification is objected to by the Ex				
10) The drawing(s) filed on is/are: a)		-		
Applicant may not request that any objection Replacement drawing sheet(s) including the		· •		
11) The oath or declaration is objected to by	· ·			
Priority under 35 U.S.C. § 119	•			
12) Acknowledgment is made of a claim for for a) All b) Some * c) None of: 1. Certified copies of the priority docu 2. Certified copies of the priority docu 3. Copies of the certified copies of the application from the International E * See the attached detailed Office action for	uments have been received. uments have been received in a e priority documents have been Bureau (PCT Rule 17.2(a)).	Application No n received in this National Stage		
Attachment(s)				
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-9 3) Information Disclosure Statement(s) (PTO-1449 or PTO/Paper No(s)/Mail Date	48) Paper No	Summary (PTO-413) (s)/Mail Date Informal Patent Application (PTO-152) 		

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DETAILED ACTION

1. Amendment filed on January 18, 2005 has been entered.

2. Claims 1-19 are presented for examination.

Response to Arguments

3. Applicant's arguments with respect to claims 1-19 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 5. Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Papaliolios (U.S. Patent No. 5,198,706).

Regarding claim 1, Papaliolios discloses a non-volatile memory cell (Figure 1) comprising:

a latch circuit (M1, M2, M3, M4) which comprises a first node and a second node (figure 1) and latches complementary data set in the first node and second node;

a first switching element (P0) which connects the first node to a first data input/output line; a second switching element (P1) which connects the second node to a second data input/output line;

a first ferroelectric capacitor (FE0) which connects the second data input/output line to the first node; and

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a second ferroelectric capacitor (FE1) which connects the first data input/output line to the second node.

Allowable Subject Matter

6. Claims 2-3 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 2-3 include allowable subject matter since the prior art made of record and considered pertinent to the applicant's disclosure does not teach or suggest the claimed limitations. Papaliolios (U.S. Patent No. 5,198,706), and others, does not teach the claimed invention having first and second inverter comprising a resistor which are serially connected between a power lines and ground.

7. Claims 4-19 are allowed.

Claims 4-19 include allowable subject matter since the prior art made of record and considered pertinent to the applicant's disclosure does not teach or suggest the claimed limitations. Papaliolios (U.S. Patent No. 5,198,706), and others, does not teach the claimed invention having a method and an apparatus of non-volatile memory device comprising a first ferroelectric capacitor and a second ferroelectric capacitor select element which are serially connected between the second data input/output line and a first node, and a second ferroelectric capacitor and a first ferroelectric capacitor select element which are serially connected between the second data input/output line and a first node.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thong Q. Le whose telephone number is 571-272-1783. The examiner can normally be reached on 8:00am-5:00pm M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hoai V. Ho can be reached on 571-272-1777. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Thong Q!Le Primary Examiner Art Unit 2827

THONG LEIP PRIMARY EXAMINER